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U.S. UTILITY Patent Application

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George Fleming Bruce Murray Don Tolsch

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IEEE 1394 link layer chip with "50" authentication and key exchange accelerator $% \left(1,0,0,0\right)$

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TERMINAL		DRAWINGS		CLAIMS ALLOWED			
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
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subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date) ;				
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